



C16/C16S-EC-402

5649

PART - B

10×5=50

BOARD DIPLOMA SUPPLEMENTARY EXAMINATION, (C-16S)

JUNE / JULY - 2020

DECE - IV SEMESTER EXAMINATION

LINEAR INTEGRATED CIRCUITS

Time : 2 Hours]

[Total Marks : 80

PART - A

5×6=30

- Instructions :
- (1) Answer any SIX questions.
  - (2) Each question carries FIVE marks.
  - (3) Each answer should not exceed more than ONE Page.

- 1 List different IC packages.
- 2 Draw the symbol of an Operational Amplifier.
- 3 Define the terms differential gain and common mode gain.
- 4 Mention the merits of active filters.
- 5 Classify the types of IC regulators.
- 6 List the different types of clippers.
- 7 Define sweep voltage.
- 8 Define lock range of PLL.
- 9 List three applications of voltage to current converter.
- 10 State the need for A/D and D/A conversion.

- Instructions :
- (1) Answer any FIVE questions.
  - (2) Each question carries TEN marks.
  - (3) Each answer should not exceed more than TWO Pages.

- 11 Explain the fabrication of transistor on monolithic IC.
- 12 Explain the non-inverting amplifier configuration of operational amplifier and derive the expression of voltage gain.
- 13 Explain the RC phase shift oscillator using operational amplifier.
- 14 Explain the operation of adjustable voltage regulator (LM 317).
- 15 Explain the principle of any clamper circuit with waveforms.
- 16 Draw the block diagram of 555 IC and explain.
- 17 Explain the working of instrumentation amplifier circuit.
- 18 Explain the operation of D/A converter using R-2R ladder network.

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